CALL FOR SPONSORSHIP

22nd - 23rd August 2024

5th India ESD Workshop
Indian Institute of Science, Bangalore, India

India ESD Workshop (InEW)

ESD reliability understanding and how to design ESD safe chips have always been highly important and crucial aspects for semiconductor design, development, testing, and manufacturing cycles. Given the absence of ESD design/testing/technology knowledge, expertise, and awareness in India, in 2015 **Indian Institute of Science (IISc), Bangalore** started efforts for industry professionals and students, which today is called **India ESD Workshop (InEW).** This conference has gathered significant interest and traction from the semiconductor industry professionals in India, with the potential to expand to other regions going forward. For example, the last edition attracted 250+ senior-level engineers from the semiconductor industry. The conference has also flourished due to its significant collaborations with industry leaders like Intel, Samsung, Texas Instruments, Western Digital, Global Foundry, Qualcomm, Synopsys, Cadence and NXP etc. The objective of the conference has always been to promote the knowledge, know-how, and expertise in ESD design, technology, and testing, in collaboration with global ESD experts. With the growing manufacturing requirements, manufacturing push in the country, upcoming fabs, an increasing number of chip design centers getting into full product cycles (full product developments), and an increasing number of chip design start-ups with efforts on end products, this expertise becomes more than relevant and justifies the need and growth of venues like InEW to continue and grow. With the current context, it would not be an exaggeration if the conference grows to 500+ professionals in the years to come.

The Upcoming Edition

The next edition of the conference, i.e. 5th India ESD Workshop is planned to be held on August 22nd - 23rd, 2024. The conference has lined up over 5 keynote/plenary talks from global ESD experts, 20+ invited/contributed presentations, riveting panel discussions from industry experts, and 40+ poster discussions from industry experts and research scholars, with a focus on the push for local manufacturing and push for design start-ups in the country. More details on the conference are available here: www.iisc-inew.org

Who Should Participate and Sponsor:

- **Category A:** Semiconductor/Chip Product Companies, Fabless Design Houses, Start-ups, Electronics Systems/Hardware Design and/or Manufacturing Firms, VLSI CAD Industry: While InEW offers your engineers an excellent forum to get equipped with ESD design, technology and testing principals, meet the leading experts from around the world and network with their peers in the semiconductor industry, it also offers an excellent opportunity to showcase your developments/efforts and promote your company across Industry peers.

- **Category B:** Semiconductor Test/Processing Equipment/Tool Manufacturers and Supply Chain Industry: InEW offers you an outstanding platform to showcase your products and promote your company across the key users, i.e. semiconductor industry and industry peers.

Package	Sponsorship Cost (INR) (excl. GST)	Sponsorship Offering (Category A Companies)	Sponsorship Offering (Category B Companies)
Silver	₹ 1,00,000	• Poster Board (in poster session) • Registration for 5 engineers	• Poster Board (in poster session) • Registration for 5 engineers
Gold	₹ 1,50,000	• Silver + • Company's logo on the website and publicity material	• Silver + • Company's logo on the website and publicity material
Platinum	₹ 2,50,000	• Gold + • 15-minute Technical Talk Slot • Stage Backdrop • 5 Additional Registrations	• Gold + • 10-minutes Product Promotion Talk • Stage Backdrop
Silicon	₹ 3,50,000	• Platinum + • Logo on the program booklet • 5 Additional Registrations	• Platinum + • Company's Logo on the lanyard and program booklet • Access to the registration data