2nd India ESD Workshop

IEW

17th March, 2017

Indian Institute of Science, Bangalore

Vision and Background: MSDLab @ IISc Bangalore (India) & ESD Association (USA) have envisioned a need to strengthen and promote ESD design activities in India through a forum what now call "India ESD Forum". IEW is an outcome of this forum with following four-fold vision. First, to increase ESD awareness among engineers and academicians working in the field of VLSI Microelectronics and would like to learn or engage into ESD protection device/circuit/product design and testing activities. Second, to bring engineers together who encounter ESD problems in their designs. Third, to promote research, development and training related to on-chip ESD device/circuit design in India. Finally, the most important, to use this model to strengthen Academia-Industry partnership in and around country.

Why ESD?
Predominant failure mechanism: It is estimated that above ~25% of component failures are due to Electrical Overstress (EOS) and Electrostatic Discharge (ESD).

Changing Technologies: Constantly changing technologies make the ESD protection design challenging. Learning from past Si technologies is no more sufficient.

Reliable Chips: For reliable operation of Integrated circuits ESD continues to be a major concern.

Venue: Faculty Hall, Main Building, IISc Bangalore.
Hosted by: MSDLab & DESE.
Reporting Time: 9:45 AM.

Organizers and sponsors

Program Schedule	Time
Opening Remarks	10:00 - 10:30 AM
Quick Recap: Past Events and Lessons Learned	10:00 - 11:30 AM
Tea Break	11:30 - 11:45 AM
Discussion – Round 1 (Topic: ESD Design Challenges in Indian Design Centers)	11:45 - 12:30 PM
Technical Talk (Topic: On-Chip ESD Verification – Available Tools and State of the art)	12:30 - 1:00 PM
Networking Lunch	1:00 - 2:30 PM
Brainstorming Session and Contributed Talks – I	2:30 - 3:30 PM
Ask the Experts	3:30 - 5:00 PM
Brainstorming Session and Contributed Talks – II	5:00 - 6:00 PM
Closing Remarks and Feedback	6:00 - 6:30 PM
Networking Hi-Tea and Poster Session	6:30 - 7:00 PM

MSDLab: http://mayank.dese.iisc.ac.in/
Workshop Details: http://iew.dese.iisc.ac.in/
Registration: http://iew.dese.iisc.ac.in/2nd-workshop-registration/
Contact: indiaesdforum@gmail.com

MSDLab
Mayank Shrivastava's Nano Devices Lab

ESD ASSOCIATION

IEEE
EDS/SSCS
Bangalore chapter

DESE
Department of Electronic Systems Engineering